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INFORMATION DISCLOSURE STATEMENT BY APPLICANT				Application Number	10/644,801-Conf. #7386
<i>(Use as many sheets as necessary)</i>				Filing Date	August 21, 2003
				First Named Inventor	Akira Shoji
				Art Unit	2819
				Examiner Name	J. B. Jeanglaude
Sheet	1	of	1	Attorney Docket Number	AIS-0008

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number Number-Kind Code ² (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear

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NON PATENT LITERATURE DOCUMENTS					
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.			²
JB/JU	CA	Yamamori et al., "NbN/TiN _x /NbN/TiN _x /NbN double-barrier junction arrays for programmable voltage standards," Applied Physics Letters, Vol. 80, No. 8, Pages 1415-1417, American Institute of Physics (February 25, 2002)			
JB/JU	CB	Kohjiro et al., "Surface Resistance of Epitaxial and Polycrystalline NbCN Films in Submillimeter Wave Region," IEEE Transactions on Applied Superconductivity, Vol. 3, No. 1, Pages 1765-1767, IEEE (March 1993)			

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Examiner Signature	<i>Jean Bruner Jeanglaude</i>	Date Considered	12/02/04
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